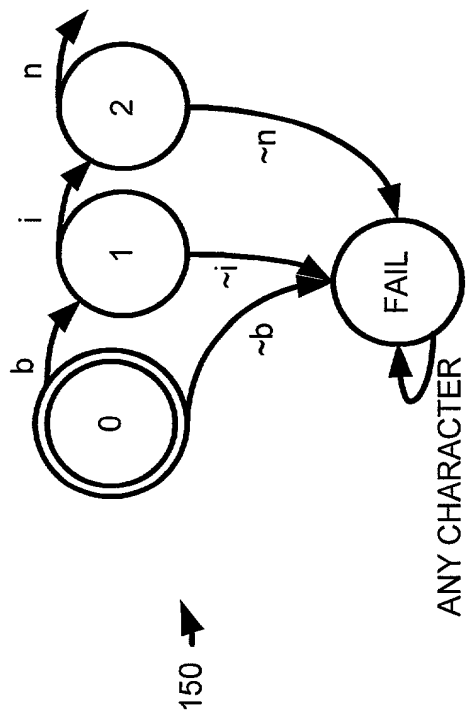


1/8



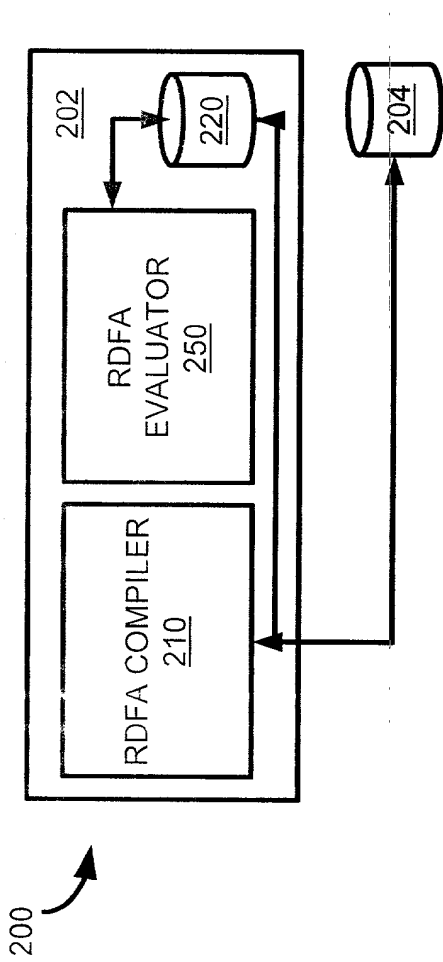


FIG. 2A

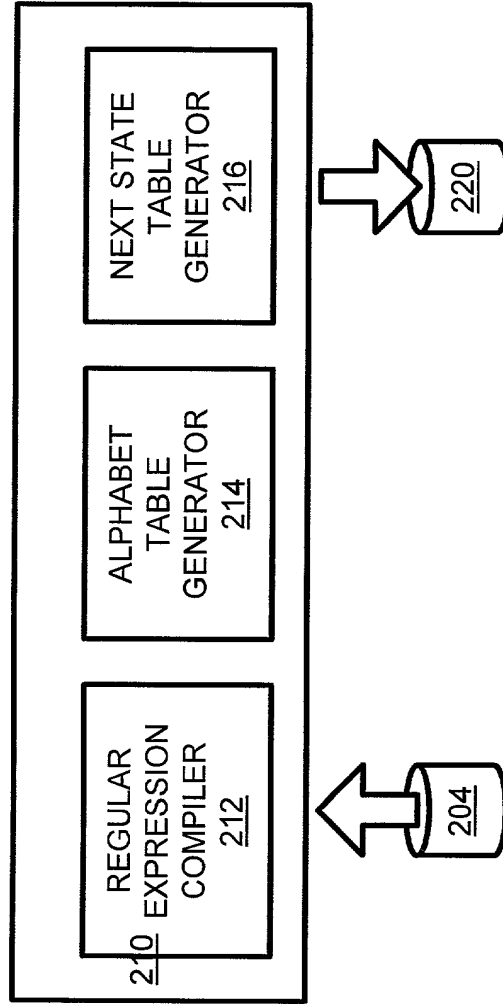
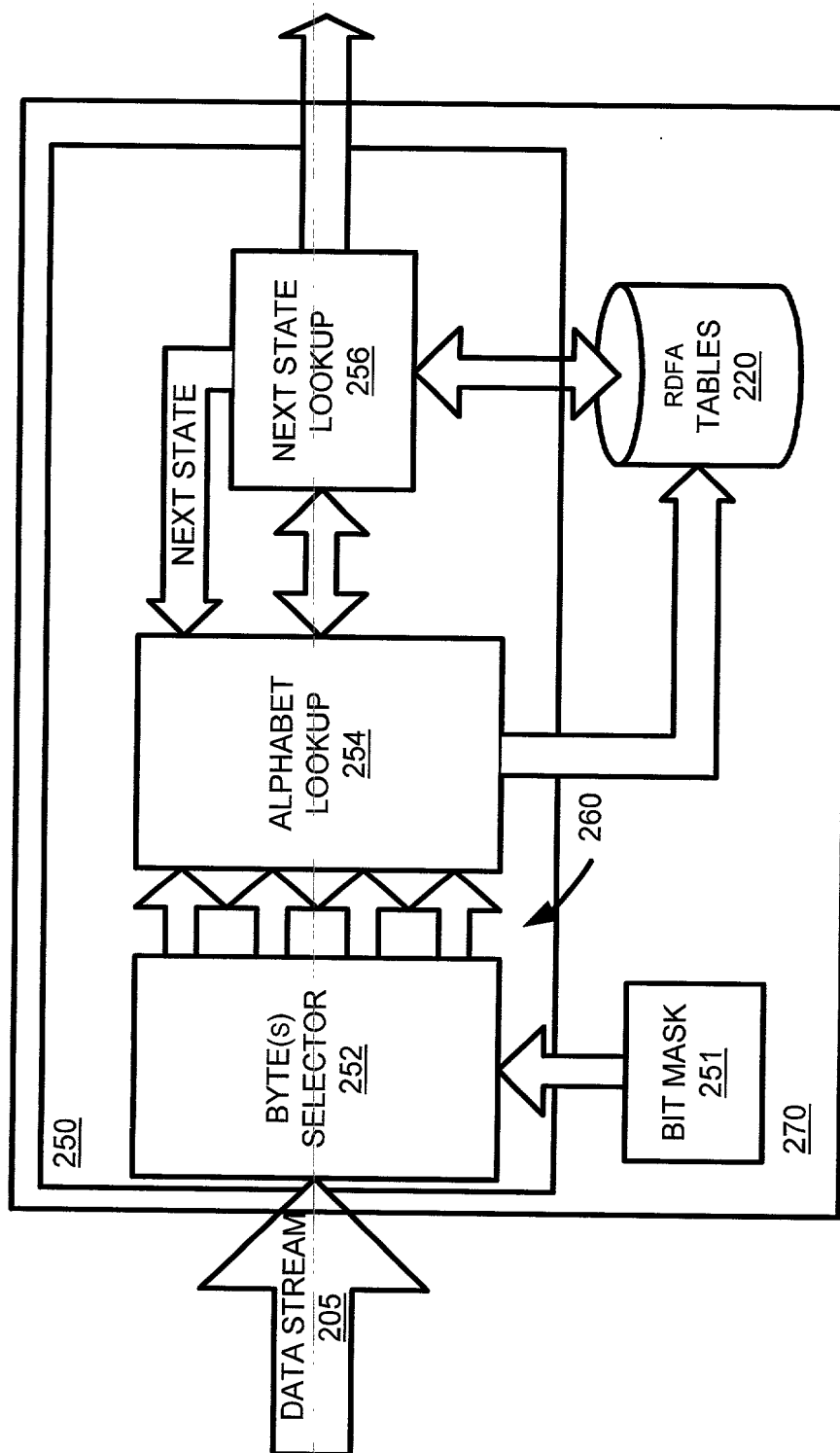


FIG. 2B



**FIG. 2C**

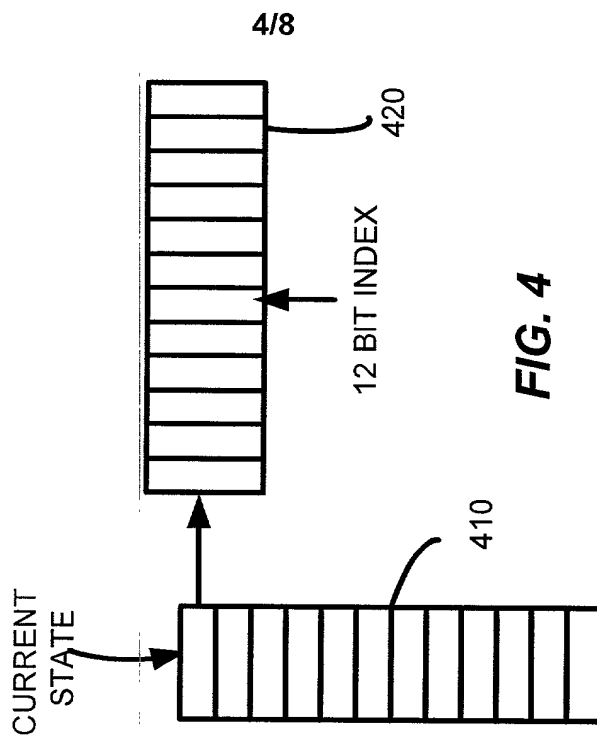
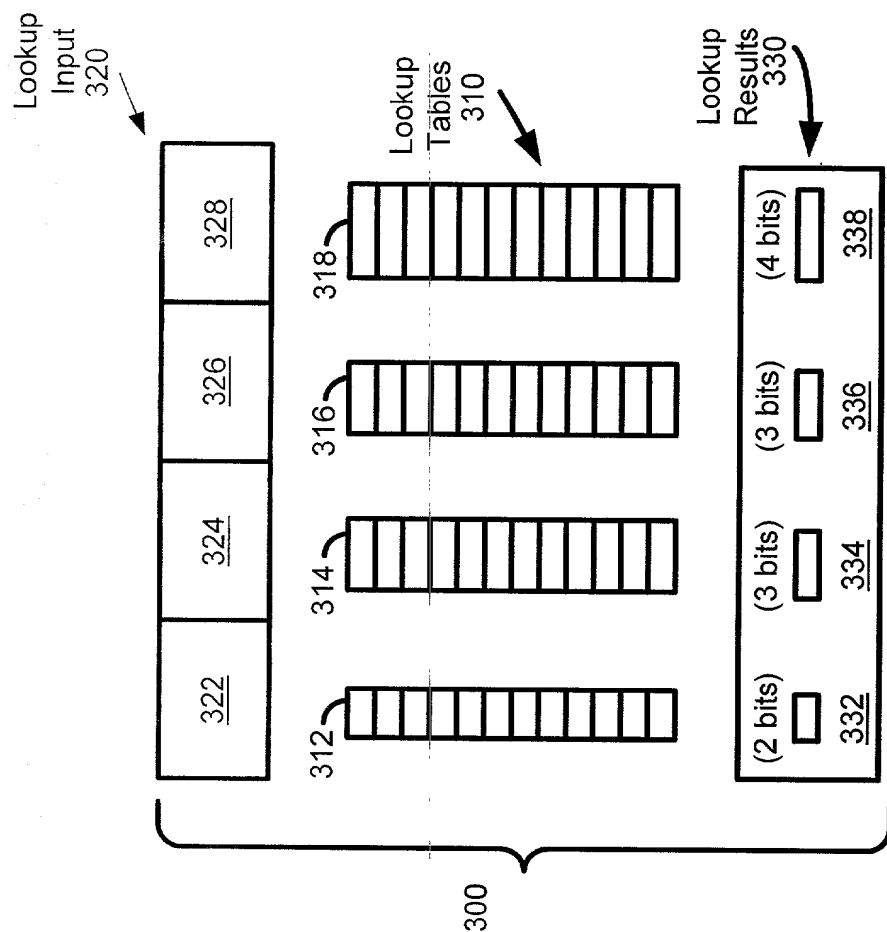
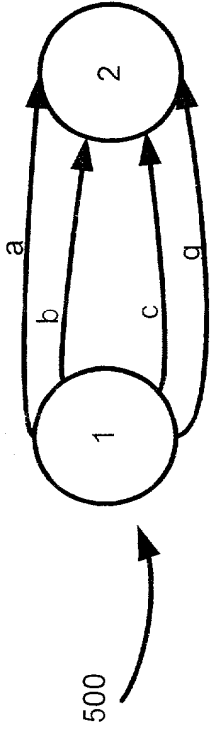


FIG. 4

FIG. 3



FIG. 5



650

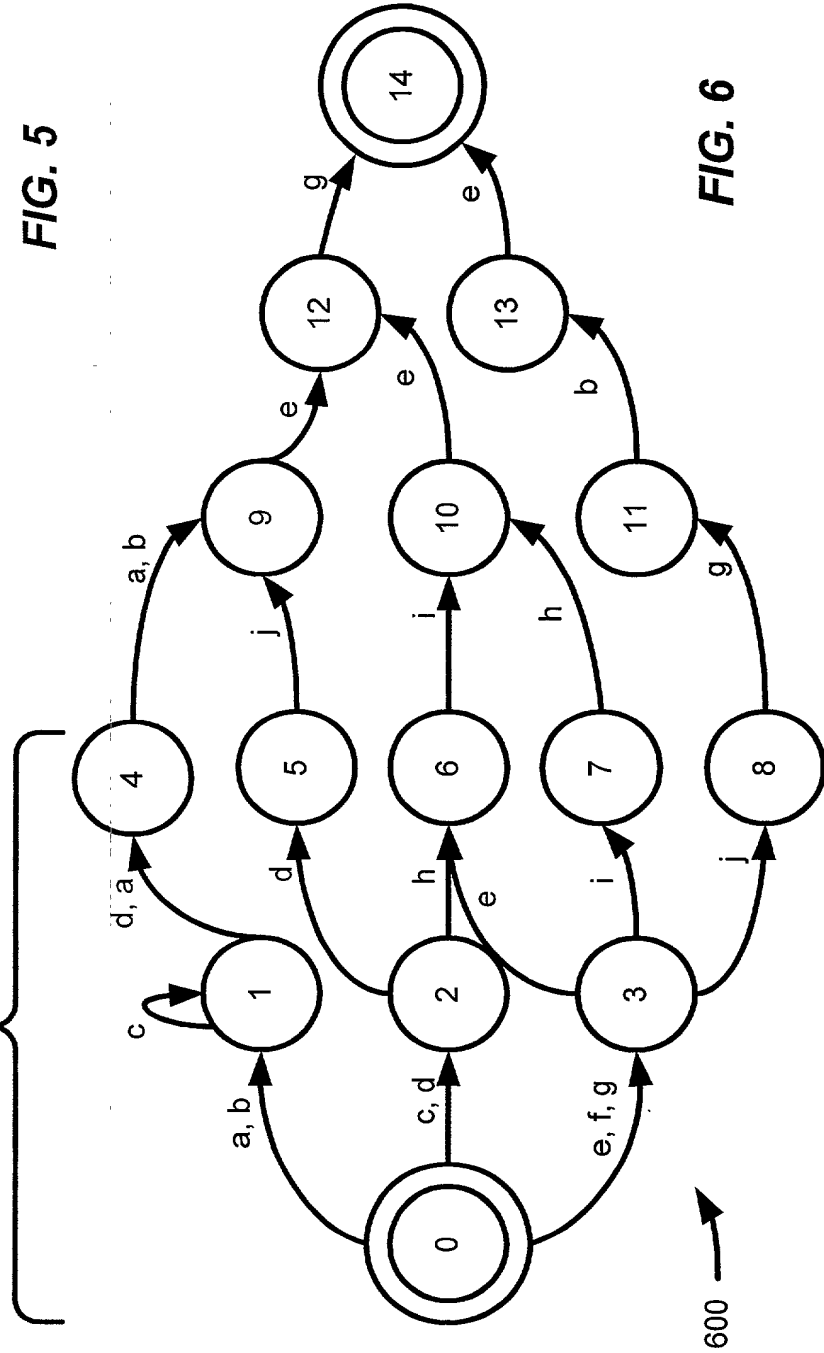


FIG. 6

600

**Fig. 6A** Illustration of number of states reachable by 2-closure.

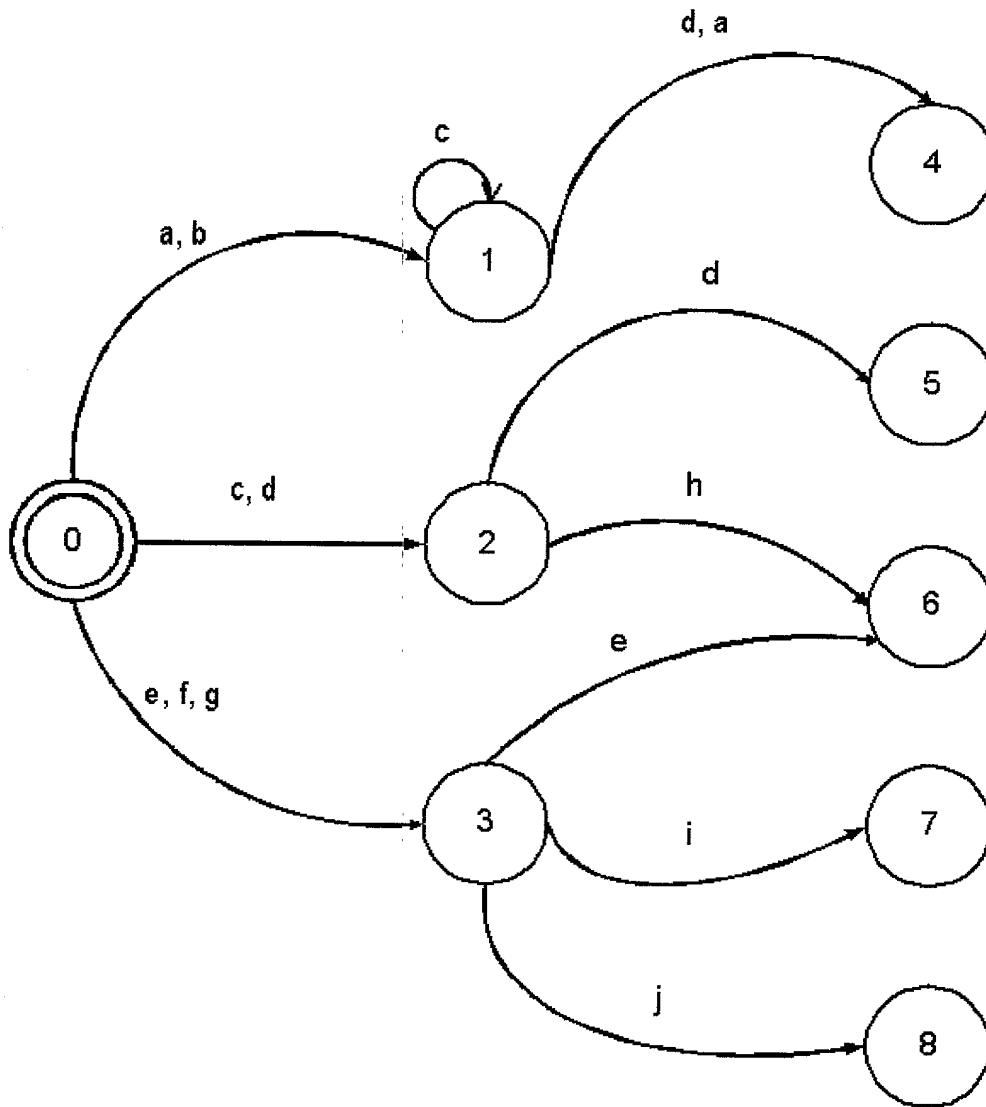


Fig. 7A DFA for processing 8 characters

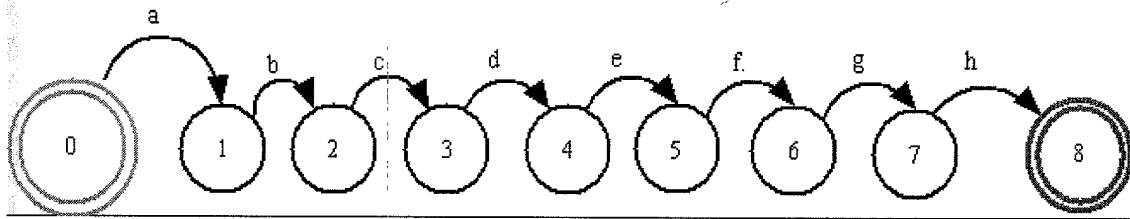


Fig 7B RDFA for processing 4 bytes in parallel.

